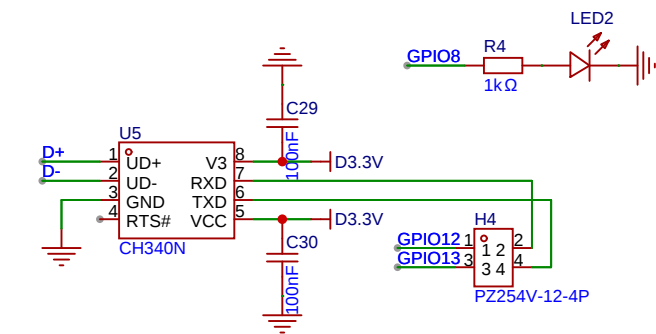
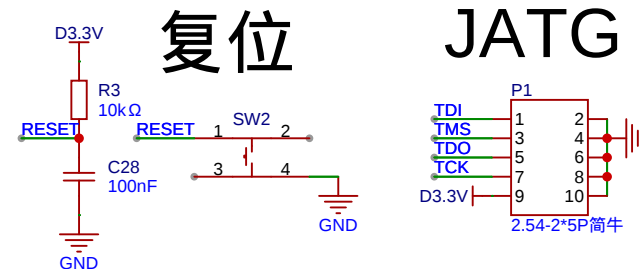


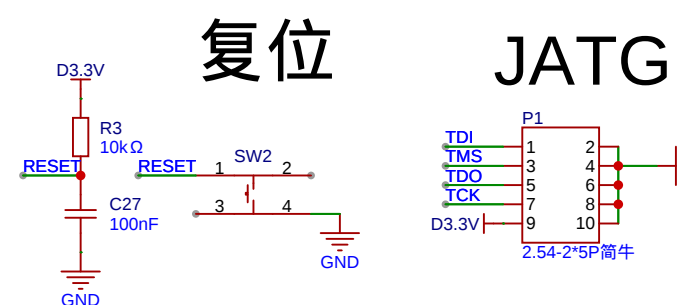
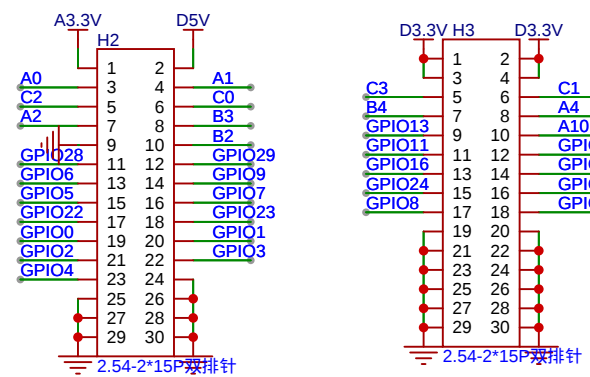
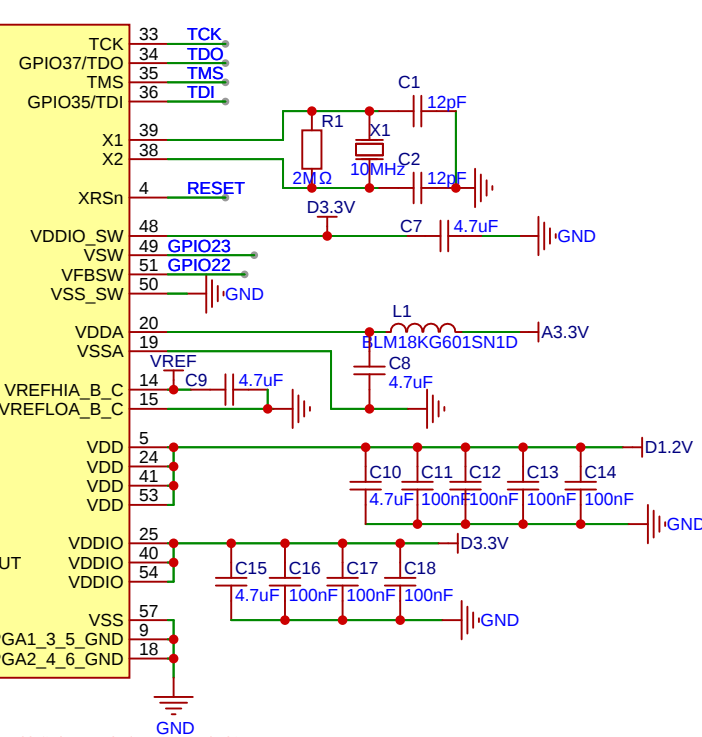
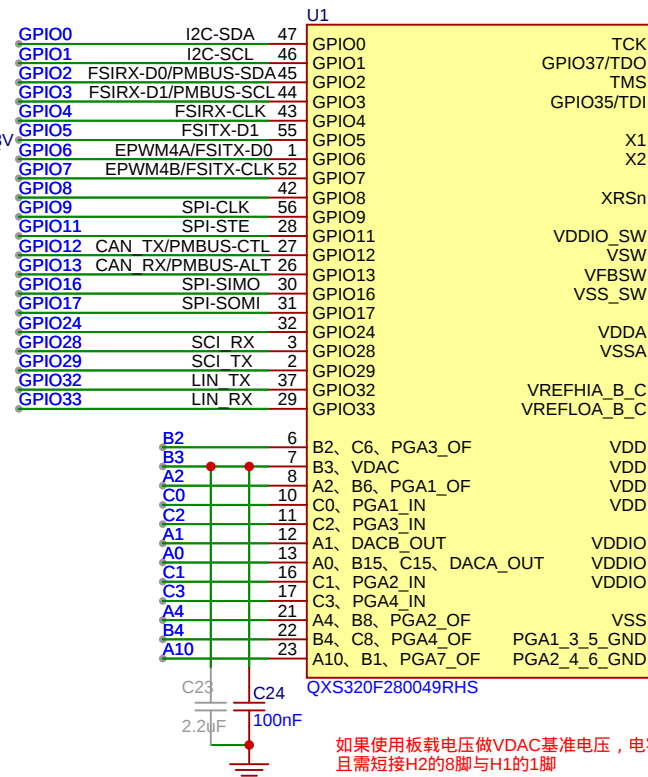
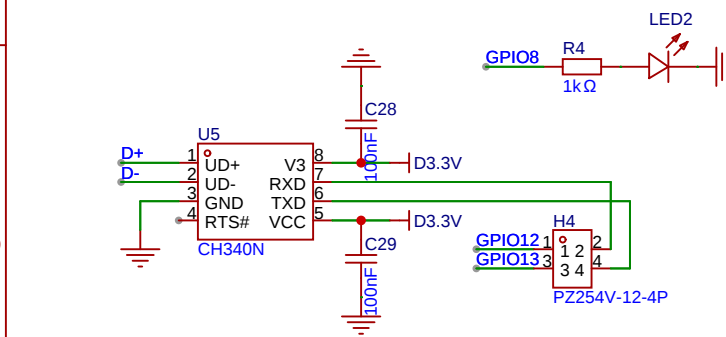
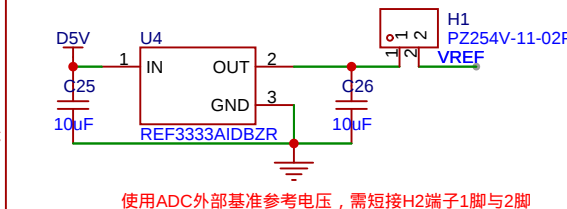
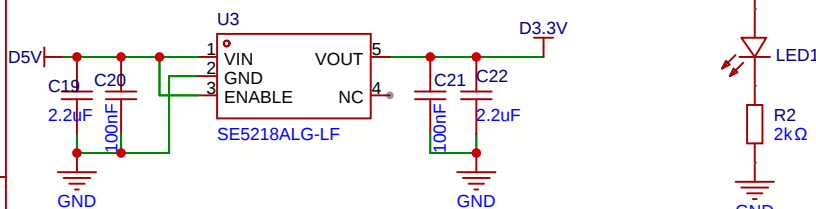


Pin	Signal	Power Plane
B2	6	B2, C6, PGA3_OF
B3	7	VDD
A2	8	B3, VDAC
C0	10	A2, B6, PGA1_OF
C2	11	C0, PGA1_IN
A1	12	C2, PGA3_IN
A0	13	A1, DACB_OUT
C1	16	A0, B15, C15, DACA_OUT
C3	17	C1, PGA2_IN
A4	21	C3, PGA4_IN
B4	22	A4, B8, PGA2_OF
A10	23	B4, C8, PGA4_OF
		PGA1_3_5_GND
		PGA2_4_6_GND



如果使用板载电压做VDAC基准电压，电容C24必须焊接且需短接H3的8脚与H1的1脚





原理图	049-56PIN-VBB		创建日期	2025-05-20	
			更新日期	2025-07-03	
板子	049-56pin-BB		图页	P1	
绘制	QXS320F280049最小系统板				
审阅					
		版本	尺寸	页	1 共 1
		V1.0	A4	嘉立创EDA	